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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group art unit: 2895
Examiner: Swapneel Chhaya

In Re PATENT APPLICATION of:

Applicant(s): Takamitsu YAMANAKA

Serial No.: 10/577,457

Filing date: April 27, 2006

For: SEMICONDUCTOR DEVICE AND ITS
MANUFACTURING METHOD

Atty. ref.: AI 409NP

)
) **RESPONSE TO**
) **ELECTION OF SPECIES**
) **REQUIREMENT**

)
) **AND**

) **RESTRICTION**
) **REQUIREMENT**

October 28, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Examiner Chhaya:

Responsive to the Restriction Requirement mailed on October 3, 2008, Applicant elects the invention of Group I, i.e., the subject matter of claims 1-4. In the Restriction Requirement, the Examiner indicated that claims 11 and 12 are also drawn to the semiconductor device. However, it is noted that claims 11 and 12 are drawn to the method of manufacturing a semiconductor device instead. The election is made without traverse.

Responsive to the Election of Species Requirement mailed on October 3, 2008, Applicant elects the embodiment of the species, as shown in Figure 1, for examination. Claims 1, 2 and 4, read on the elected species. The election is made without traverse.

Examination of the application on the merits is respectfully requested.

Respectfully submitted,

October 28, 2008
Date

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